

Electronic Acknowledgement Receipt

EFS ID:	3095865
Application Number:	10755042
International Application Number:	
Confirmation Number:	8665
Title of Invention:	Integrated chip package structure using silicon substrate and method of manufacturing the same
First Named Inventor/Applicant Name:	Mou-Shiung Lin
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Filer:	Winston Hsu
Filer Authorized By:	
Attorney Docket Number:	MEGP0004USA1
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes) /Message Digest	Multi Part /.zip	Pages (if appl.)
1	Amendment Submitted/Entered with Filing of CPA/RCE	NP072-AM.pdf	82833	no	1
			e5e8169fda2117b53d0dcaa31499afdb1b631597		
Warnings:					
Information:					
2	Claims	NP072-CL.pdf	100936	no	8
			ebde39d7bf0ae922135a3f5a2e2ca7b6ef047bcd		
Warnings:					
Information:					
3	Applicant Arguments/Remarks Made in an Amendment	NP072-RM.pdf	182125	no	14
			753a197bc9554c665e6e17caf1110e65637b397d		
Warnings:					
Information:					
4	Request for Continued Examination (RCE)	sb0030e_fill.pdf	699389	no	3
			901f94df456ff1b2fcb9c38760443acc25ee7696		
Warnings:					
Information:					
5	Fee Worksheet (PTO-06)	fee-info.pdf	8205	no	2
			739fcbd2b2c6b8fb9469074b08e9c594784a01be		
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